

WINTERLOGIC PARTNERS WITH DAOUXILICON FOR SALES AND DISTRIBUTION IN KOREA

Roseville, MN – December 2, 2009 – WinterLogic Inc., a leading supplier of Verilog fault simulation tools, today announced that it has expanded its sales efforts to the Republic of Korea with an exclusive distribution agreement with DaouXilicon Technology Co., Ltd.

“The growth of the Korea semiconductor market over the past years represents a significant opportunity for our products. We are very excited to have DaouXilicon working with us to provide local sales to customers in Korea.” stated Jason Campbell, president of WinterLogic.

“WinterLogic’s functional fault simulation tools are gaining a reputation for significant reduction of test development time for electronic designs.” remarked Denis Kim, EDA Sales Team Senior Manager of DaouXilicon. “We are delighted to be associated with this leading-edge, industry-proven product for the Korean market.”

About Z01X Fault Simulator

Based on WinterLogic’s proprietary C2 algorithm, Z01X is the fastest functional fault simulator available. Z01X is also the only Verilog fault simulator that has the capability of simulating stuck-at, transition and bridge faults. Z01X supports the full Verilog IEEE 1364-2001 language and accepts WGL, STIL, VCD or testbench stimulus.

About DaouXilicon

Daou Xilicon has been distributing high-tech software for semiconductor design and testing since 1997 through its offices at DaeHyun B/D 4F, 273-1 SeoHyuDong, Bundang-Gu, Seongnam-Si, Kyungki-Do 463-824 Korea. For more information, visit www.daouxilicon.com.

About WinterLogic

Established in 2005, WinterLogic specializes in helping companies achieve their DPM targets and reduce tester time. We have special expertise in high-speed fault simulators and fault management environments for Verilog designs. Our unique technologies dramatically improve the performance of fault simulation. Our proprietary expert test system uses “testability analysis” to provide optimum test ordering, eliminate redundant tests and report on uncovered areas of the design before fault simulation begins. For more information, visit www.winterlogic.com.

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